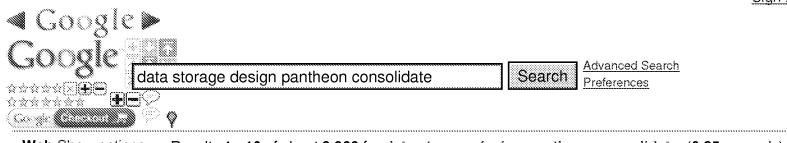
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3. GCP Pantheon - Koios White Board

Nov 3, 2008 ... The details of each sub-use case will be discussed in the **design** section below. .... How long should the white board store the **data**? ... either as separate lists or as a **consolidated** (union or intersection set?) list. ... The KoiosWB **Storage** Manager: developed as an implementation of the GCP ... **pantheon**.generationcp.org/index.php?option=com... - <u>Cached - Similar</u>

4. web links

Commercial **Consolidated**, 1380 **Pantheon** Way, #255. Commercial Maintenance Contracting, Inc. ... Icon **Design** Group, 1380 **Pantheon** Way, #150 ... Promissor, Inc. 1380 **Pantheon** Way, #190. Public **Storage**, 16639 San Pedro 210-494-9654 ...

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An ultra high-efficiency NASnetwork attached **storage** appliance for archiving ... Cypress cuts the energy consumption associated with **data storage** byup to 80 ... enterprise-class virtualization and **consolidation** platform withup to 70 ... boosting **design** integrity and time-to-market for all high-speed **designs**. ... resources.bnet.com/.../high+availability+and+sun+solaris.html - <u>Cached</u> - <u>Similar</u>

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www.tjoos.com/.../Pantheon-of-Dagoba-Organic-Chocolate - Cached - Similar

# 8. Tripos, AstraZeneca, WorldFusion, Ariadne, Boston University ...

Oct 10, 2008 ... The project will be based on Muse, a de novo **design** product that Tripos plans to release later this year as part of its **Pantheon** computer-aided drug **design** platform. ... IGSP's **data storage** infrastructure has grown from 4 TB to 300 TB ... **storage** to **consolidated**, network-attached NetApp FAS systems. ...

www.genomeweb.com/.../tripos-astrazeneca-worldfusion-ariadne-boston-university-nonlinear-dynamics-neta - <u>Cached - Similar</u>

### 9. Barker, Richard M. STORAGE NETWORKING ESSENTIALS (Adobe Reader ...

The inside scoop on a leading-edge **data storage** technology The rapid growth ... all facets of **storage** networking, explaining how a SAN can help **consolidate** ...

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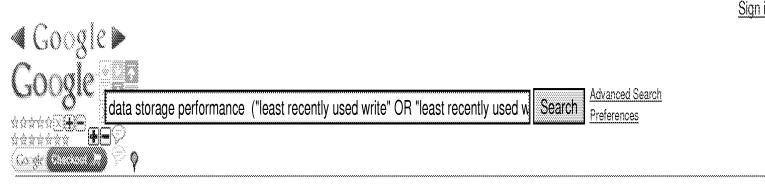
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1 is a block diagram of a data storage environment including a performance simulation system configured for carrying out method steps of this invention; ...

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2. "Hardware Considerations". In: Real-Time Systems Design and ...

Of course, wait states degrade overall systems **performance**, ...... Code and **data storage**. Ferroelectric RAM. 40 ns. 64 Mbytes. Various ..... replacement algorithm (e.g., first-in-first out; least recently used), write policy ... doi.wiley.com/10.1002/0471648299.ch2 - Similar

3. [PDF] Automatic Parallelization and Minimization of Communication Costs ...

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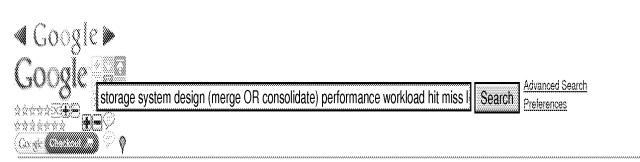
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Design objective for mass storage systems include cost, performance, ..... wishes to merge or consolidate data storage systems as described above. ....

Read Hit: Percentage of I/Os that reread data from cache Read Miss: .... It is good for the link bandwidth to be at least as large as the average write workload. ...

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consolidate their server and storage resources into data .... or Least Frequently Used (LFU) replacement algorithms ... If one of them misses the cache (Hit-Miss), then the ..... array, a write-intensive workload is introduced at the 10 minute mark. .... that a balanced storage system design with the right ... www.soe.ucsc.edu/~ari/Ari-SANCache-MSST06.pdf - Similar

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3. Characteristics of I/O traffic in personal computer and server ...

IBM Systems Journal issue 42-2, Storage Systems - Feature paper. ... and to revalidate rules of thumb that are useful for systems design and sizing. ..... This finding suggests that read performance in personal system workloads could ..... cache miss ratios assuming a write-back cache with the least-recently-used ...

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4. The design of a lockup-free cache for high-performance ...

accesses always appear to hit at the cache since a write to a word ..... ber of miss buffers used on average. It is possible to estimate ... switches, the cache can adapt to such workload changes if ... this scheme is probably best implemented in large systems with at least several dozens of processors. ...

ieeexplore.ieee.org/iel2/222/1696/00044672.pdf?amumber=44672 - Similar

by C Scheurich - 1988 - Cited by 19 - Related articles - All 4 versions

5. Chapter 18 MEASURING PERFORMANCE

the number of minutes until the first cache **hit/miss** after a breakdown ..... of any cache replacement policy that is based on the **Least** Recently **Used** (LRU) ... only characterize Web proxy **workload** but also discuss the **design** of a light- ... than a version of Squid that ran on the high **performance** XFS file **system**. ... www.springerlink.com/index/t3n362jt53213n6j.pdf - Similar

## 6. HashCache: Cache Storage for the Next Billion

Cache **storage system design** has been shaped by this class of users, leading to **design** decisions that fa- ... suitable for a range of **workloads** in developing regions. ..... disk seek rate, the **hit** rate, the **miss** rate, and the **write** ..... alytical models, we expect a **performance** of at **least** 320 ...

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# 7. [PDF] ASR: Adaptive Selective Replication for CMP Caches

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memory **system performance** when it reduces the average L1 **miss** latency. The following equation describes ..... been a local **hit** if replication was increased. If so, ASR increments ... counters [32] to estimate which blocks are **least** recently **used**. .... **performance** to a wider variety of **workloads**. **Storage** Overhead. ...

www.cs.wisc.edu/multifacet/papers/micro06\_asr.pdf - Similar

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## 8. Design of the 64-bit Option for the Oracle7 Relational Database ...

A cache **miss**, in contrast, is the single largest contributor to ... For instance, one or more of the **least** recently **used** buffers may be evicted from ..... a series of **performance** tests for a DSS-class **workload** of the Oracle7 **system**, ... scans and sorts/**merge** into smaller work units, and executes them concurrently. ... www.hpl.hp.com/hpiournal/dtj/vol8num4/

# 9. [PDF] Sun Storage 7110, 7210, and 7410 Unified Storage Systems

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Traditionally, combining compute and **storage workloads** was extremely complex ..... DRAM provides a local buffer to accelerate Flash **write performance** and maintain ..... File **system** DNLC Entries. Raw. DNLC Accesses. **Hit/Miss**, Raw .... All of the Sun servers **used** in Sun **Storage** 7000 Unified **Storage Systems** include an ...

www.dewpoint.com/.../SunStorage7000Architecture\_WhitePaper\_31Oct08.pdf - Similar

# 10. Geeking with Greg: 08/01/2008 - 09/01/2008

A near the end of the talk, Jitendra contrasted the techniques **used** for face **... systems** that makes them better suited to data-warehousing **workloads**", **...** that summarizes a Google talk at SIGMOD 2008 on a **storage system** they **...**. It returns the stored top-k results if a query is a **hit** or reports a **miss** otherwise. **...** glinden.blogspot.com/2008 08 01 archive.html - Cached - Similar

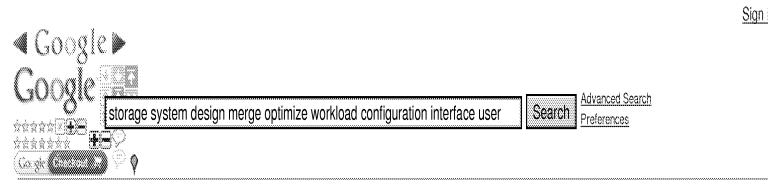
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1. Intelligent Storage: Cross-Layer Optimization for Soft Real-Time ...

In current narrow interface design, the ability to pass valuable infor- ... human-centered management and configuration overheads. Advancements in ..... To perform workload-aware system optimization, it is mandatory that the in- ... ple, the request merge and separation of device driver bottom-half. When the ...

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2. Future Generation Computer Systems: Distributed smart disks for I ...

A case at hand is the Virtual Interface Architecture (VIA), a user-level memory-mapped .... and storage performance at the lowest possible system configuration cost. ... Recent research on network attached storage systems suggests the use of .... To evaluate ARM FSC workload for the proposed SD architecture, ...

linkinghub.elsevier.com/retrieve/pii/S0167739X05001196 - Similar by SC Chiu - 2006 - Related articles

3. Method and system for simulating performance on one or more data ...

Design objective for mass storage systems include cost, performance, ... of disk array data storage systems concerns optimizing the storage capacity while ..... An exemplary interface user screen 406 for the tool is further shown in FIG. ... 21 shows the user configuration selected after the user clicks "calculate. ...

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4. [PDF] BORG: Block-reORGanization for Self-optimizing Storage Systems

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evaluation of BORG, a self-optimizing storage system ..... user and the SVN server workloads. Further, for each of .... In this section, we present the **design** details of BORG ..... the latter two phases, the I/O scheduler **merge** ratio and .... 14% and 18% for cumulative and partial

## configuration. It ...

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# 5. Borg: Self-Optimizing Block Storage.

Jan 13, 2009 ... BORG: Block-reORGanization for Self-optimizing Storage Systems .... The office and developer workloads are single-user workloads. ..... The complexity of the merge process increases if two vertices (either within the same ... the actual data movement to realize the new configuration of the BOPT. ...

www.usenix.org/events/fast/tech/full\_papers/.../index.html - <u>Cached - Similar</u> by M Bhadkamkar - <u>Related articles - All 3 versions</u>

# 6. Physical Design Refinement: The "Merge-Reduce" Approach

meet the constraints on the physical **design** (such as **storage**, update cost, or ... designs for indexes and materialized views in a database **system**. ..... re-**optimize** the inferred **workload** under the new **configuration** CF. We ...

www.springerlink.com/index/a1051pj524uw173w.pdf - <u>Similar</u> by N Bruno - 2007 - Cited by 16 - Related articles

# 7. Index tuner for given workload - Patent 6266658

The **system** of claim 25 and further comprising a **merge** module that merges indexes ..... An object may support an **interface** by providing executable code for the ..... **configuration** R' is then evaluated for the **workload** by **optimizing** each query in ... The **configuration** may be used by a **user** or **system** administrator to ...

www.freepatentsonline.com/6266658.html - <u>Similar</u> by C Lewis - 2001 - <u>Cited by 3 - Related articles</u>

# 8. [PS] Self-Tuning Technology in Microsoft SQL Server

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the self-tuning advances in the query engine and the **storage** engine respectively ... load of the **system**. An important component of physical database **design** is selecting .... Microsoft. SQL Server. **Optimize** Query. Simulate **Configuration**. **Workload** ... ample, if a **merge** join obtains its data from two sort operations, ...

sites.computer.org/debull/99june/msft.ps - Similar by S Chaudhuri - Cited by 50 - Related articles - All 14 versions

# 9. Fault Recovery Designs for Processor-Embedded Distributed Storage ...

the high-level architectural **design** of the smart **storage system**. A disk-based smart node (SN) ... the **system** parameters are used for code **optimization**. The ... smart **storage system** for various DB and non-DB **workloads**. Aggregate. IndexScan ... **User interface**. Cost models.

# **System** catalogs. (for database). SD **system** ...

ieeexplore.ieee.org/iel5/9687/30571/01410747.pdf?arnumber... - <u>Similar</u> by SC Chiu - Cited by 1 - Related articles - All 9 versions

# 10. [DOC] SciDAC Preproposal - Systems Software Center

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The **user interface** will be modeled on existing queue **systems** such as PBS and will ..... The ideal scalable **systems design** environment would include a suite of simulator ... (60 months) Administrative tools to automate **workload configuration** ..... Linux and Windows clusters, the mass **storage system** and networking. ...

www.scidac.org/ScalableSystems/proposal.doc - Similar

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S Palacharla, RE Kessler - Computer Architecture, 1994., Proceedings the 21st ..., 1994 - leeexplore leee.org

... codes show much lower primary cache miss rates than ... to any particular memory system

design details. ... Table 1: Benchmark Characteristics This table describes the ...

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RH Saavedra, AJ Smith, University of ... - IEEE Transactions on Computers, 1995 - Citeseer

... As part of this work, we describe the **design** and perfor- ... The SPEC (Fortran) **benchmark** work ... cache miss penalty, although the measurement was made at the machine ...

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### Data cache performance of supercomputer applications

D Callahan, A Porterfield - Proceedings of the 1990 ACM/IEEE conference on ..., 1990 - portal acm.org

... The benchmark programs used for this work in-cluded ... LINPACKD - the standard LINPACK bench- mark [Don881 ... for the program is affected by the cache structure, the ...

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#### Cache performance of operating system and multiprogramming workloads- washing workloads.

A Agarwal, J Hernessy, M Horowitz - ACM Transactions on Computer Systems (TOCS), 1988 - portal acm.org

... Thus a viable design alternative is to overcome ... If in the Appendix summarizes benchmark trace characteristics ... We present cache miss rate results throughout this ...

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#### Measuring cache and TLB performance and their effect on benchmark run times- \*\times a

AJ Smith, RH Saavedra, AJ Smith - IEEE Transactions on Computers, 1995 - eprints.kfupm.edu.sa

... We show that the SPEC benchmark results on these machines ... 3.2.5. Write Buffers A cache, in which the update ... in the write buffer, it can in some designs (eg the ...

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#### Informed prefetching and caching- comused them

RH Patterson, GA Gibson, E Ginting, D ... - Proceedings of the fifteenth ACM symposium on ..., 1995 - portal acm.org

... Prefetching strategies are needed to "parallelize" these workloads. ... management based on informed prefetching and caching. ... Benchmark applications and ...

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#### [PDF] \*Systematic computer architecture prototyping

TM Conte - 1992 - Citeseer

 $\dots$  Evolution of energy for gcc **benchmark** during execution  $\dots$  of the overall **design** process  $\dots$  emphasison **cache**-basedmemorysystems and superscalar processor components of work  $\dots$ 

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#### Improving the efficiency of UNIX buffer caches- <a href="https://www.edu.sa/">https://www.edu.sa/</a> (908)

A Braunstein, M Riley, J Wilkes - ACM SIGOPS Operating Systems Review, 1989 - portal acm.org

... The primary XMF **design** goal was to minimize the **READ** ... operations, and to collect hardware **cache** and TLB ... to be highly repeatable: the **benchmark** programs were ...

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### Wisconsin architectural research tool set- wisc.edu [TXT]

MD Hill, JR Larus, AR Lebeck, M Talluri, DA ... - ACM SIGARCH Computer Architecture News, 1993 - portal acm.org

... Smith, "Cache Performance of the SPEC Benchmark Suite," to ... scheme, and adaptation of this design to a ... by the least recently used (LRU) cache replacement policy ...

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#### Optimizing instruction cache performance for operating systemintensive workloads-

J Torrellas, C Xia, R Daigle - First IEEE Symposium on High-Performance Computer ..., 1995 - ieeexplore.ieee.org

... affected by the degree of placement optimization for the ... To gain insight into the cache performance of operating sys ... use this insight later to design the code ...

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### Cache-DASD storage design for improving system performance

CP Grossman - IBM Systems Journal, 1985 - research.ibm.com

... Through an iterative process, a set ... RPQ, illustrates a different ap- proach.l2 In this design, the performance ... to one of these slots and read into cache storage ...

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#### Optimizing the instruction cache performance of the operating system-

J Torrellas, C Xia, Rt. Daigle - IEEE Transactions on Computers, 1998 - leeexplore.ieee.org

... expose these three localities, we design and evaluate ... code, Section 4 presents our optimizing algorithm, and ... per-processor 16- Kbyte on-chip instruction cache. ...

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#### Approaching a machine-application bound in delivered performance onscientific code

WH Mangione-Smith, TP Shih, SG Abraham, ... - Proceedings of the IEEE, 1993 - leeexplore leee.org

... bottlenecks that are critical to the design of today ... effort is spent in program optimization, and enough ... dispatch bandwidth of the Instruction-Cache Unit (ICU). ...

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### Informed prefetching and caching- \*consecuta (POF)

RH Patterson, GA Gibson, E Ginting, D ... - Proceedings of the lifteenth ACM symposium on ..., 1995 - portal acm.org

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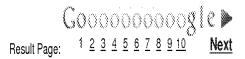
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#### Trace-driven memory simulation: A survey- \*psu.edu [907]

RA Uhlig, TN Mudge - ACM Computing Surveys, 1997 - portal.acm.org ... bus of a host computer while it runs a **workload**. ... to collect address traces for TLB and **cache** simulation. ... of 32 bits each) necessitated the **design** of circuitry ...

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CP Grossman - IBM Systems Journal, 1985 - research.ibm.com

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... bottlenecks that are critical to the **design** of today ... effort is spent in program

optimization, and enough ... dispatch bandwidth of the Instruction-Cache Unit (ICU). ...

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A Braunstein, M Riley, J Wilkes - ACM SIGOPS Operating Systems Review, 1989 - portal acm.org

... The primary XMF design goal was to minimize the ... obtained from measuring "micro-level"

performance parameters. ... from the file buffer cache READ results were ...

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TM Conte - 1992 - Citeseer

... of convergence of the overall design process ... the number of iterations and enlarging ... emphasison cache-basedmemorysystemsand superscalarprocessor componentsofwork ...

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#### Evaluation of closely coupled systems for high performance databaseprocessing

E Rahm - Distributed Computing Systems, 1993., Proceedings the ..., 1993 - Iseexplore isee org

... GEM and magnetic disks with or without disk cache. ... important workload type for evaluating the performance of transaction and ... workload generation and allocation ...

file:///C/Documents%20and%20Settings/spatel3/My%20Documents/e-Red%20Fo...used%20write%20read%20(pending%20OR%20queue)%20-%20Google%20Scholar.htm (1 of 2)7/9/2009 2:04:11 PM

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H Sharangpani, K Arora - IEEE Micro, 2000 - membres.lycos.tr

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DC Burger - 1998 - cs.utexas.edu

... issues in computer system **design**. ... We show that the Indirect **Cache** works synergistically with the traffic **optimization** techniques described in ...

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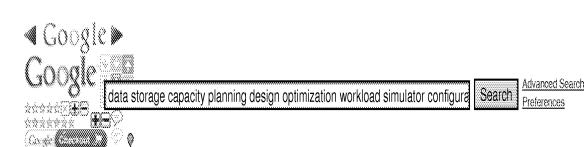
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consolidation and capacity planning. Data requirements for capacity management differ ... change the workload or configuration and to try to improve performance prior to ... consolidation or workload optimization. Integrated simulation modeling using .... traffic, minimize data storage requirements at the user end, ...

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Requirement: Automated Infrastructure **Capacity Planning** for Primary IT Services .... use of **simulation** software make it possible to investigate **capacity planning** ... Analysis of the monitored **data** may identify areas of the **configuration** .... **design** tasks fit in the overall scope and sequence of the **consolidation**, ...

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Oct 27, 2008 ... configuration and capacity planning for solaris servers ... performance by design computer capacity planning .... Lans) • Consolidation of servers, storage, SANs Availability • Data Availability Network .... being Read From "Server A" Traffic Analysis Capacity Planning Workload Analysis Diagnostics ...

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## 8. SYSTOR 2009 - The Israeli Experimental Systems Conference

We successfully integrated STAMP into a power-aware capacity planning tool to ... to enabling efficient data center design and true I/O consolidation. .... on write amplification analytically and by simulation assuming workloads of ..... migration and network configuration, which allows better optimization for ... www.haifa.ibm.com/conferences/systor2009/abstracts.shtml - Cachad - Similar

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At what workload does the user response time fail to meet performance requirements? ... that had service-level agreements in place and processes for capacity planning; ... Data collection planning. Here, we determined metrics and sources of ... in a performance model, using a popular simulation modeling product. ... doi.leeecomputersociety.org/10.1109/MITP.2003.1235607 - Similar by A Spellmann - 2003 - Cited by 2 - Related articles - All 7 versions

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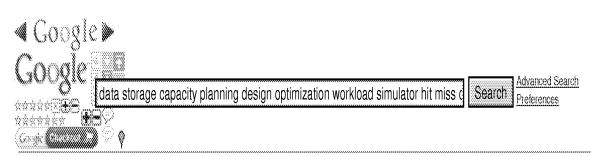
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new **configuration** above, for example, **workload** X's bottleneck might shift ..... Success stories in **capacity planning**: Mathematical models have tra- ... to know the consequences of migrating **data** from **storage**-node A to **storage**- ...... the **data** path. The request may **hit** in the NFS server cache or **miss** and have ...

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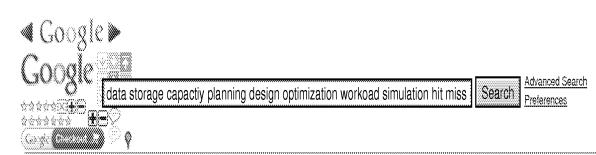
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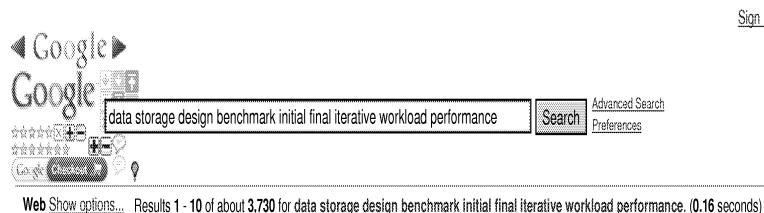
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Carefully consider **performance** issues during the **initial design** phase, ... Scalability is a system's ability to process more **workload**, .... Configuring the **initial** system architecture is a largely **iterative** process. .... for **storage** of aggregates and pre-joined **data** by clustering and materialized view functions. ...

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tions that reduce the energy (**Iterative** Improvement – II) ver- sus the SA algorithm. ... **initial data** placement before the **workload** is executed. 3.1. Solution State ... The nodes in the graph represent the **data storage** and compu- .... is similar to the schema specified by the **benchmark** and the ...

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